



PCIe Gen5 E1.S To M.2 Adapter Card

PCI5-AD-E1M2-1X4

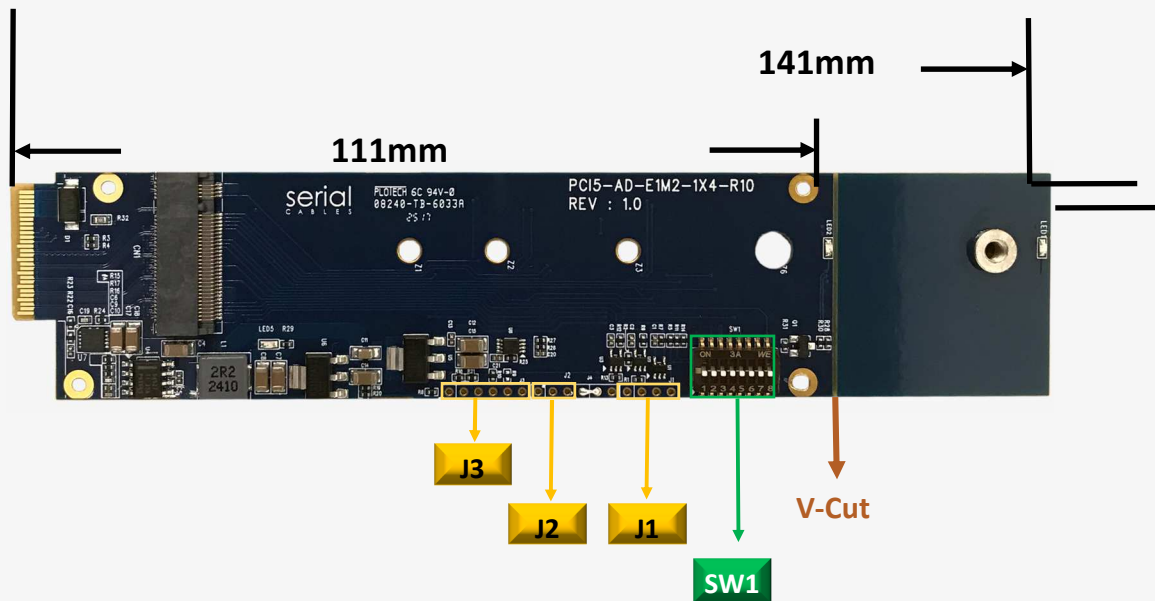
User's Manual

REV: 1.0

July. 2025



REV	Change history	Date of Release
1.0	New created	July. 2025



Headers	Descriptions						
J3	Header populated M.2 side-band signals.						
	Pins in M.2	54	73	68	30	8	
	Signals	M2_PEWAKE#	M2_VIOCFG	M2_SUSCLK	M2_PLA_S3#	M2_PLN#	GND
	Pins in Header	1	2	3	4	5	6
J2	Header for M.2 MFG I2C accessing.						
	Pins in M.2	58	56				
	Signals	M2_MFG_CLOCK	M2_MFG_DATA	GND			
	Pins in Header	1	2	3			
J1	Header for M.2 SMBus accessing.						
	Pins in M.2		44	42	40		
	Signals	GND	M2_ALERT#	M2_SMDAT	M2_SMCLK		
	Pins in Header	1	2	3	4		



The V-cut is reserved for 22110 size M.2 SSD support. It can be break off, support up to 2280 M.2 SSD and put into E1.S 9.5mm, 15mm and 25mm cages.



SW1		
Pins	Pins	Descriptions
16	15	Connect 1.8V to M2_VIO1V8
14	13	NC
12	11	NC
10	9	ON: Disable M.2 3.3V output
8	7	ON: Connect M2_CLKREQ# to E1.S CLKREQ#
6	5	ON: Set M.2 PERST# to "L"
4	3	ON: Connect M2_PRSENT# to E1.S PRSNT0#
2	1	ON: Set M.2_PWRDIS_0 to "H"